

**RC4156/RC4157****High Performance Quad Operational Amplifiers****Description**

The 4156 and 4157 are monolithic integrated circuits, consisting of four independent high performance operational amplifiers constructed with an advanced epitaxial process.

These amplifiers feature guaranteed AC performance which far exceeds that of the 741 type amplifiers.

Also featured are excellent input characteristics and guaranteed low noise, making this device the optimum choice for audio, active filter and instrumentation applications. The 4157 is a decompensated version of the 4156 and is AC stable in gain configurations of -5 or greater.

**Features**

- ◆ Unity gain bandwidth for 4156 — 3.5 MHz
- ◆ Unity gain bandwidth for 4157 — 19 MHz
- ◆ High slew rate for 4156 — 1.6 V/ $\mu$ S
- ◆ High slew rate for 4157 — 8.0V/ $\mu$ S
- ◆ Low noise voltage — 1.4  $\mu$ V<sub>RMS</sub>
- ◆ Indefinite short circuit protection
- ◆ No crossover distortion

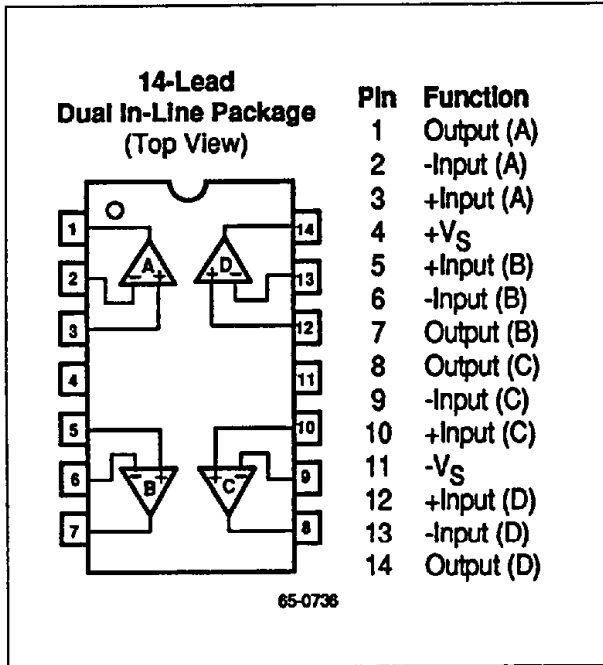
# RC4156/RC4157

RAYTHEON/ SEMICONDUCTOR

64E D —

## Connection Information

## Ordering Information



Part Number	Package	Operating Temperature Range
RC4156N	N	0°C to +70°C
RC4156M	M	0°C to +70°C
RC4156D	D	0°C to +70°C
RC4157N	N	0°C to +70°C
RC4157M	M	0°C to +70°C
RM4156D	D	-55°C to +125°C
RM4156D/883B	D	-55°C to +125°C

Notes:  
 /883B suffix denotes Mil-Std-883, Level B processing  
 N = 14-lead plastic DIP  
 D = 14-lead ceramic DIP  
 M = 14-lead plastic SOIC

## Absolute Maximum Ratings

Supply Voltage .....	±20V
Differential Input Voltage .....	30V
Input Voltage <sup>1</sup> .....	±15V
Output Short Circuit Duration <sup>2</sup> .....	Indefinite
Storage Temperature Range .....	-65°C to +150°C
Operating Temperature Range	
RM4156/4157 .....	-55°C to +125°C
RC4156/4157 .....	0°C to +70°C
Lead Soldering Temperature	
(DIP; 60 sec) .....	+300°C
(SO-14; 10 sec) .....	+260°C

Notes:  
 1. For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.  
 2. Short circuit to ground on one amplifier only.

**Thermal Characteristics**

	14-Lead Plastic SO-14	14 Lead Plastic DIP	14 Lead Ceramic DIP
Max. Junction Temp.	+125°C	+125°C	+175°C
Max. P <sub>D</sub> T <sub>A</sub> <50°C	300mW	468mW	1042mW
Therm. Res. θ <sub>JC</sub>	—	—	60°C/W
Therm. Res. θ <sub>JA</sub>	200°C/W	160°C/W	120°C/W
For T <sub>A</sub> >50°C Derate at	5.0 mW/°C	6.25 mW/°C	8.38 mW/°C

**Electrical Characteristics**

(V<sub>S</sub> = ±15V, R<sub>M</sub> = -55°C ≤ T<sub>A</sub> ≤ +125°C, R<sub>C</sub> = 0°C ≤ T<sub>A</sub> ≤ +70°C)

Parameters	Test Conditions	RM4156/4157			RC4156/4157			Units
		Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage	R <sub>S</sub> ≤ 10 kΩ			5.0			6.5	mV
Input Offset Current				75			100	nA
Input Bias Current				320			400	nA
Large Signal Voltage Gain	R <sub>L</sub> ≥ 2 kΩ, V <sub>OUT</sub> ±10V	25			15			V/mV
Output Voltage Swing	R <sub>L</sub> ≥ 2 kΩ	±10			±10			V
Supply Current			10			10		mA
Average Input Offset Voltage Drift			5.0			5.0		μV/°C

Linear

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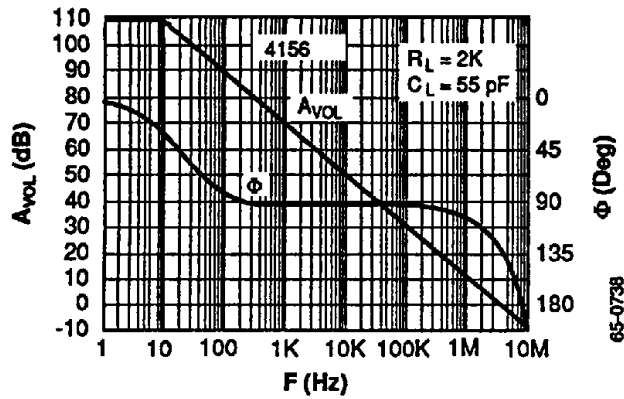
## Electrical Characteristics

( $V_S = \pm 15V$  and  $T_A = +25^\circ C$  unless otherwise noted)

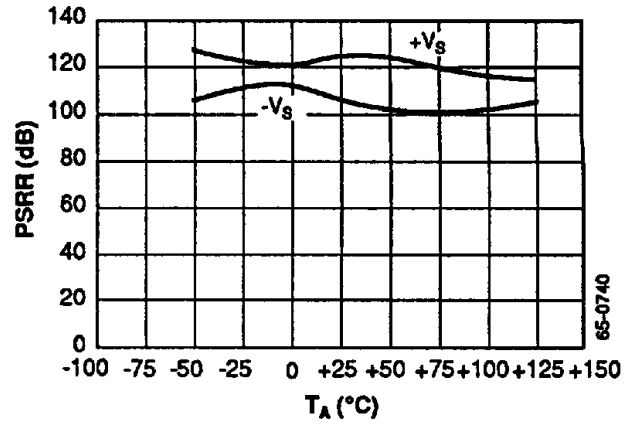
Parameters	Test Conditions	RM4156/4157			RC4156/4157			Units
		Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage	$R_S \leq 10\text{ k}\Omega$		0.5	3.0		1.0	5.0	mV
Input Offset Current			15	30		30	50	nA
Input Bias Current			60	200		60	300	nA
Input Resistance			0.5			0.5		M $\Omega$
Large Signal Voltage Gain	$R_L \geq 2\text{ k}\Omega$ , $V_{OUT} \pm 10V$	50	100		25	100		V/mV
Output Voltage Swing	$R_L \geq 10\text{ k}\Omega$	$\pm 12$	$\pm 14$		$\pm 12$	$\pm 14$		V
	$R_L \geq 2\text{ k}\Omega$	$\pm 10$	$\pm 13$		$\pm 10$	$\pm 13$		V
Input Voltage Range		$\pm 12$	$\pm 14$		$\pm 12$	$\pm 14$		V
Output Resistance			230			230		$\Omega$
Short Circuit Current			25			25		mA
Common Mode Rejection Ratio	$R_S \leq 10\text{ k}\Omega$	80			80			dB
Power Supply Rejection Ratio	$R_S \leq 10\text{ k}\Omega$	80			80			dB
Supply Current (All Amplifiers)	$R_L = \infty$		4.5	5.0		5.0	7.0	mA
Transient Response (4156)								
Rise Time			60			60		nS
Overshoot			25			25		%
Slew Rate		1.3	1.6		1.3	1.6		V/ $\mu$ S
Unity Gain Bandwidth (4156)		2.8	3.5		2.8	3.5		MHz
Phase Margin (4156)	$R_L = 2\text{ k}\Omega$ , $C_L = 50\text{ pF}$		50			50		%
Transient Response (4157)								
Rise Time	$A_V = -5$		50			50		nS
Overshoot			25			25		%
Slew Rate		6.5	8.0		6.5	8.0		V/ $\mu$ S
Unity Gain Bandwidth (4157)	$A_V = -5$	15	19		15	19		MHz
Phase Margin (4157)	$A_V = -5$ , $R_L = 2\text{ k}\Omega$ , $C_L = 50\text{ pF}$		50			50		%
Power Bandwidth	$V_{OUT} = 20V_{P-P}$	20	25		20	25		kHz
Input Noise Voltage	$F = 20\text{ Hz to } 20\text{ kHz}$		1.4	2.0		1.4	2.0	$\mu$ V <sub>RMS</sub>
Input Noise Current	$F = 20\text{ Hz to } 20\text{ kHz}$		15			15		pA <sub>RMS</sub>
Channel Separation			108			108		dB

Typical Performance Characteristics

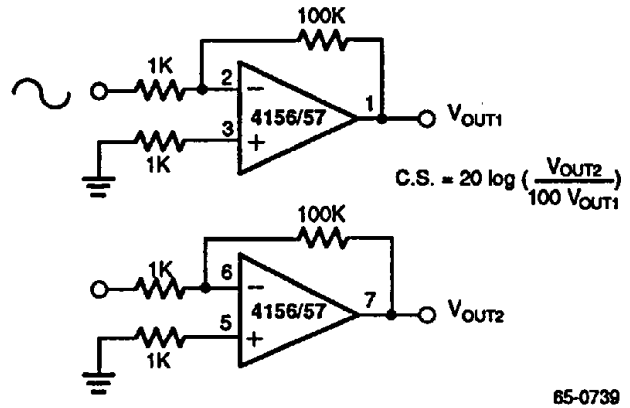
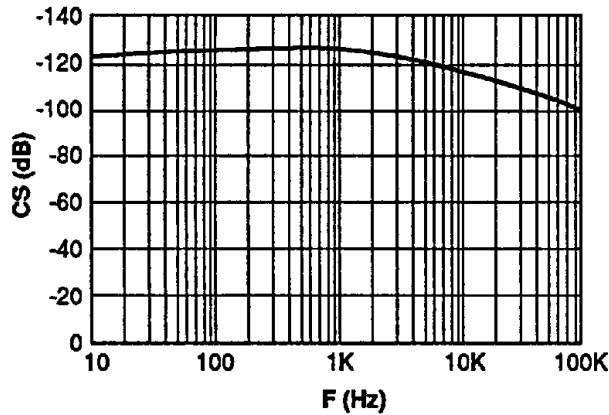
Open Loop Gain, Phase vs. Frequency



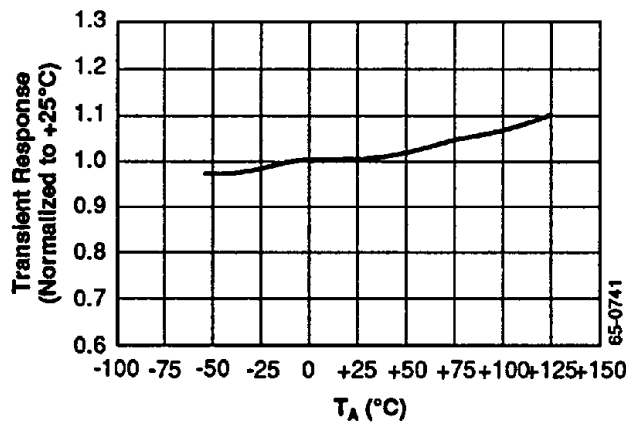
PSRR vs. Temperature



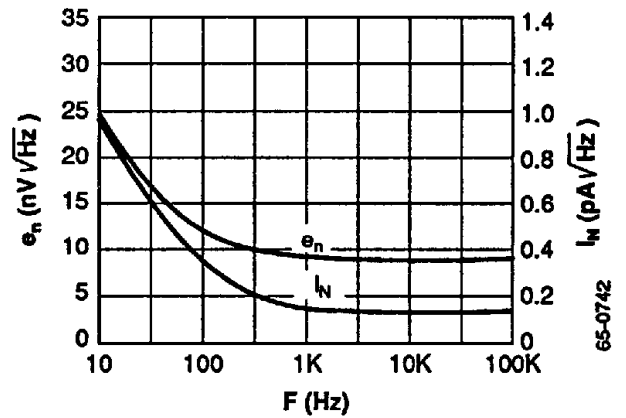
Channel Separation vs. Frequency



Transient Response vs. Temperature



Input Noise Voltage, Current Density vs. Frequency



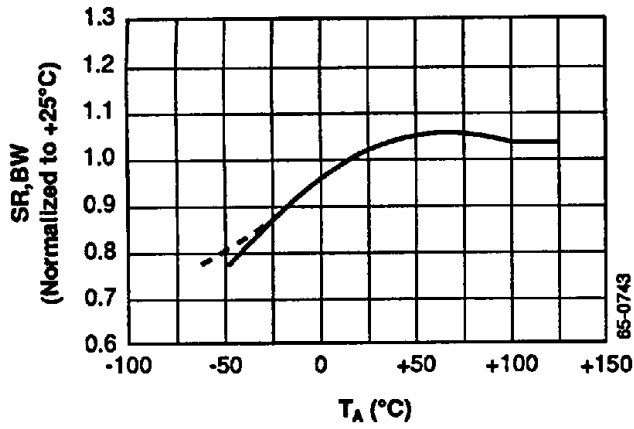
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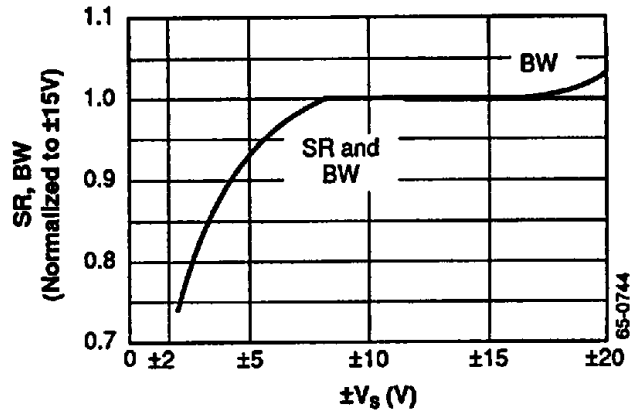
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## Typical Performance Characteristics (Continued)

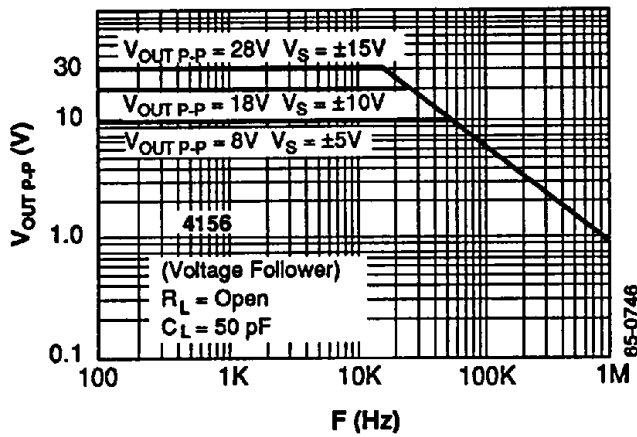
Slew Rate, Bandwidth vs. Temperature



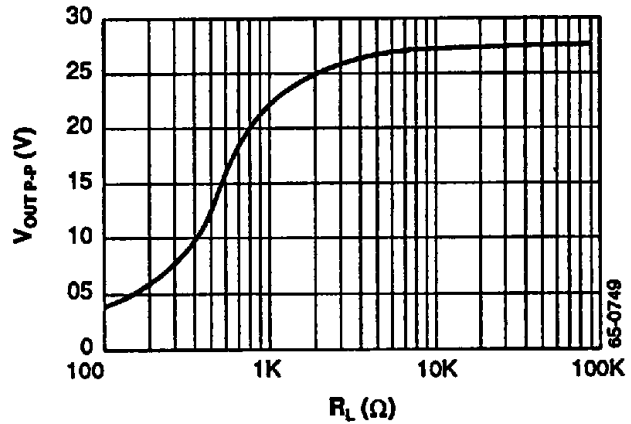
Slew Rate, Bandwidth vs. Supply Voltage



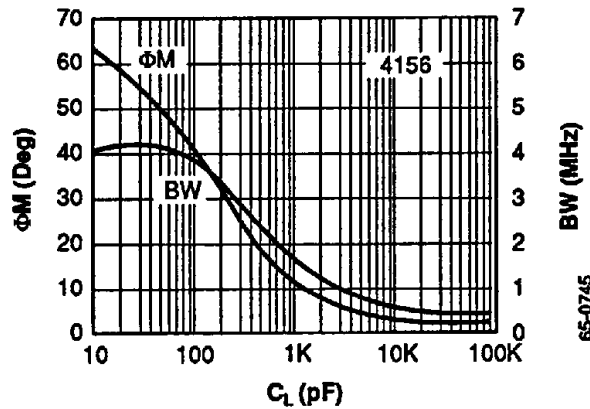
Output Voltage Swing vs. Frequency



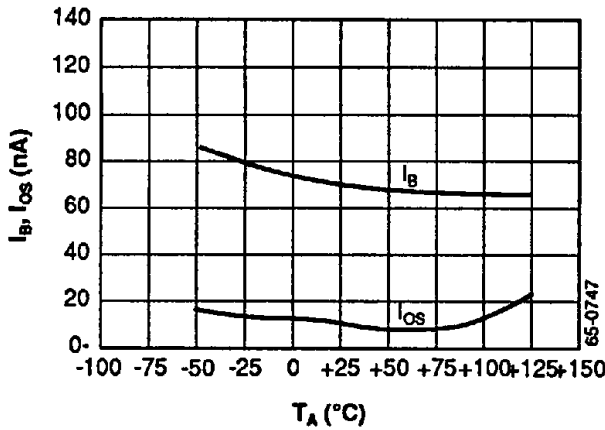
Output Voltage Swing vs. Load Resistance



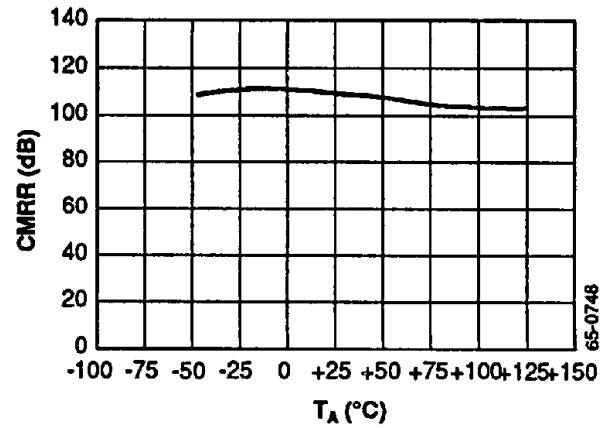
Small Signal Phase Margin, Unity Gain Bandwidth vs. Load Capacitance



Input Bias, Offset Current vs. Temperature



CMRR vs. Temperature



### Applications

The 4156 and 4157 quad operational amplifiers can be used in almost any 741 application and will provide superior performance. The higher unity gain bandwidth and slew rate make it ideal for applications requiring good frequency response, such as active filter circuits, oscillators and audio amplifiers.

The following applications have been selected to illustrate the advantages of using the Raytheon 4156 and 4157 quad operational amplifiers.

#### Triangle and Square Wave Generator

The circuit of Figure 1 uses a positive feedback loop closed around a combined comparator and integrator. When power is applied the output of the comparator will switch to one of two states, to the maximum positive or maximum negative voltage. This applies a peak input signal to the integrator, and the integrator output will ramp either down or up, opposite of the input signal. When the integrator output (which is connected to the comparator input) reaches a threshold set by R1 and R2, the comparator will switch to the opposite polarity. This cycle will repeat endlessly, the integrator charging positive then negative, and the comparator switching in a square wave fashion.

The amplitude of  $V_2$  is adjusted by varying R1. For best operation, it is recommended that R1 and  $V_R$  be set to obtain a triangle wave at  $V_2$  with  $\pm 12V$  amplitude. This will then allow A3 and A4 to be used for independent adjustment of output-offset and amplitude over a wide range.

The triangle wave frequency is set by C0, R0, and the maximum output voltages of the comparator. A more symmetrical waveform can be generated by adding a back-to-back Zener diode pair as shown in Figure 2.

An asymmetric triangle wave is needed in some applications. Adding diodes as shown by the dashed lines is a way to vary the positive and negative slopes independently.

The frequency range can be very wide and the circuit will function well up to about 10 kHz. The square wave transition time at  $V_1$  is less than 21  $\mu S$  when using the 4156.

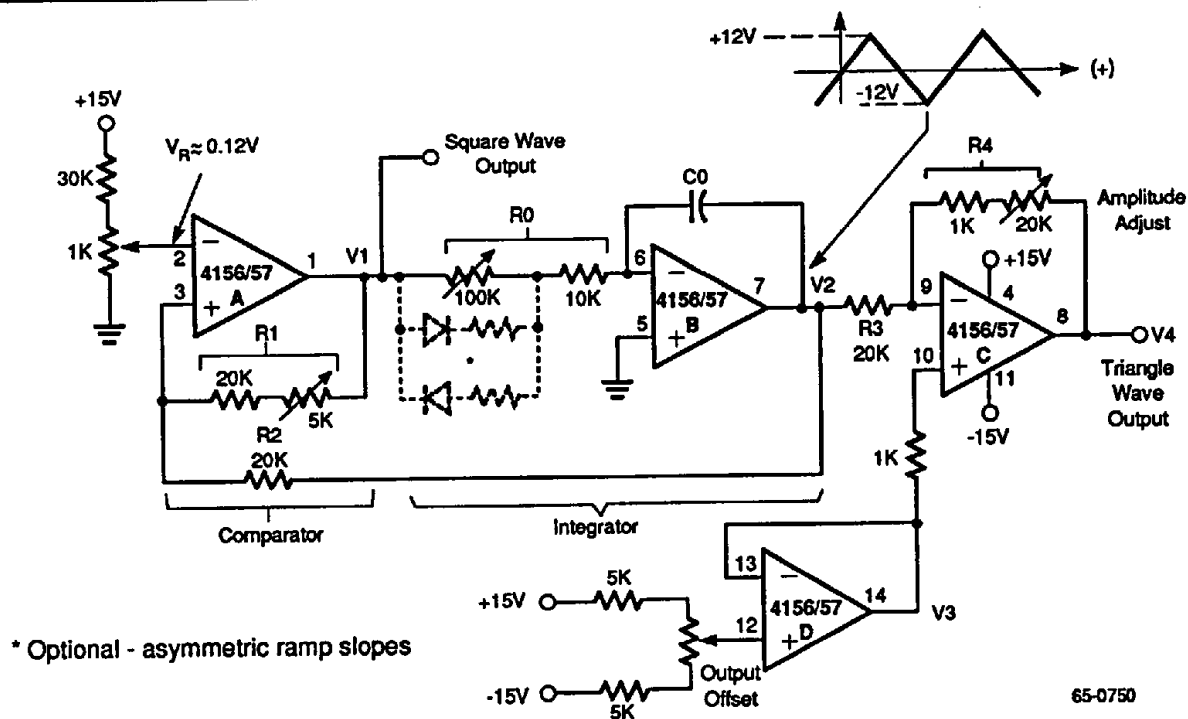


Figure 1. Triangle and Square Wave Generator

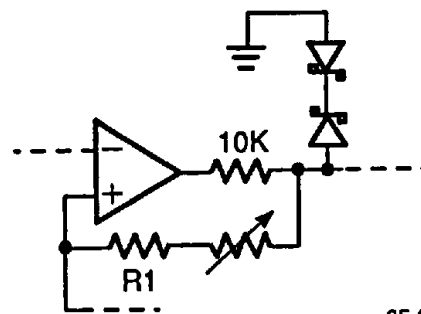


Figure 2. Triangle Generator — Symmetrical Output Option

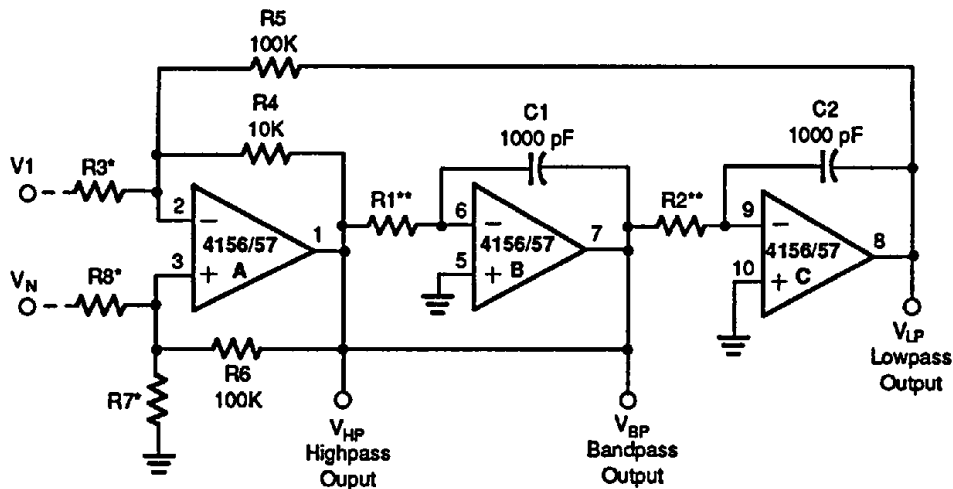
## Active Filters

The introduction of low-cost quad op amps has had a strong impact on active filter design. The complex multiple-feedback, single op amp filter circuits have been rendered obsolete for most applications. State-variable active-filter circuits using three to four op amps per section offer many advantages over the single op amp circuits. They are relatively insensitive to the passive-component tolerances and variations. The Q, gain, and natural frequency can be independently adjusted. Hybrid construction is very practical because resistor and capacitor values are relatively low and the filter parameters are determined by resistance ratios rather than by single resistors. A

generalized circuit diagram of the 2-pole state-variable active filter is shown in Figure 3. The particular input connections and component-values can be calculated for specific applications. An important feature of the state-variable filter is that it can be inverting or non-inverting and can simultaneously provide three outputs: lowpass, bandpass, and highpass. A notch filter can be realized by adding one summing op amp.

The 4156 was designed and characterized for use in active filter circuits. Frequency response is fully specified with minimum values for unity-gain bandwidth, slew-rate, and full-power response. Maximum noise is specified.





\* Input connections are chosen for inverting or non-inverting response. Values of R3,R7,R8 determine gain and Q.  
 \*\* Values of R1 and R2 determine natural frequency.

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Figure 3. 2 - Pole State-Variable Active Filter

Output swing is excellent with no distortion or clipping. The 4156 provides full, undistorted response up to 20 kHz and is ideal for use in high-performance audio and telecommunication equipment.

In the state-variable filter circuit, one amplifier performs a summing function and the other two act as integrators. The choice of passive component values is arbitrary, but must be consistent with the amplifier operating range and input signal characteristics. The values shown for C1, C2, R4, R5 and R6 are arbitrary. Pre-selecting their values will simplify the filter tuning procedures, but other values can be used if necessary.

The generalized transfer function for the state-variable active filter is:

$$T(s) = \frac{a_2s^2 + a_1s + a_0}{s^2 + b_1s + b_0}$$

Filter response is conventionally described in terms of a natural frequency  $\omega_0$  in radians/sec, and Q, the quality of the complex pole pair. The filter parameters  $\omega_0$  and Q relate to the coefficients in T(s) as:

$$\omega_0 = \sqrt{b_0} \text{ and } Q = \frac{\omega_0}{b_1}$$

The input configuration determines the polarity (inverting or non-inverting), and the output selection determines the type of filter response (lowpass, bandpass, or highpass).

Notch and all-pass configurations can be implemented by adding another summing amplifier.

Bandpass filters are of particular importance in audio and telecommunication equipment. A design approach to bandpass filters will be shown as an example of the state-variable configuration.

### Design Example — Bandpass Filter

For the bandpass active filter (Figure 4) the input signal is applied through R3 to the inverting input of the summing amplifier and the output is taken from the first integrator ( $V_{BP}$ ). The summing amplifier will maintain equal voltage at the inverting and non-inverting inputs (see equation on next page).

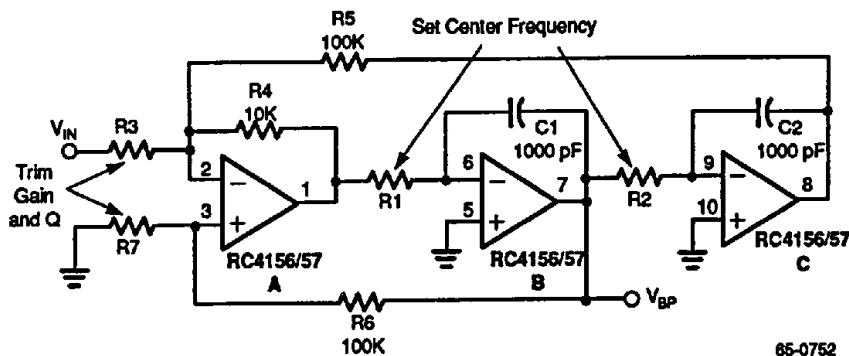


Figure 4. Bandpass Active Filter

$$\frac{\frac{R3R5}{R3 + R5}}{R4 + \frac{R3R5}{R3 + R5}} V_{HP}(s) + \frac{\frac{R3R4}{R3 + R4}}{R5 + \frac{R3R4}{R3 + R4}} V_{LP}(s) + \frac{\frac{R4R5}{R4 + R5}}{R3 + \frac{R4R5}{R4 + R5}} V_{IN}(s) + \frac{R7}{R6 + R7} V_{BP}(s)$$

These equations can be combined to obtain the transfer function:

$$V_{BP}(s) = -\frac{1}{R1C1S} V_{HP}(s) \text{ and } V_{LP}(s) = -\frac{1}{R2C2S} V_{BP}(s)$$

$$\frac{V_{BP}(s)}{V_{IN}(s)} = \frac{\frac{R4}{R3} \frac{1}{R1C1} S}{S^2 + \frac{R7}{R6 + R7} \left(1 + \frac{R4}{R5} + \frac{R4}{R3}\right) \left(\frac{1}{R1C1}\right) S + \left(\frac{R4}{R5}\right) \left(\frac{1}{R1C1R2C2}\right)}$$

Defining  $1/R1C1$  as  $\omega_1$ ,  $1/R2C2$  as  $\omega_2$ , and substituting in the assigned values for  $R4$ ,  $R5$ , and  $R6$ , then the transfer function simplifies to:

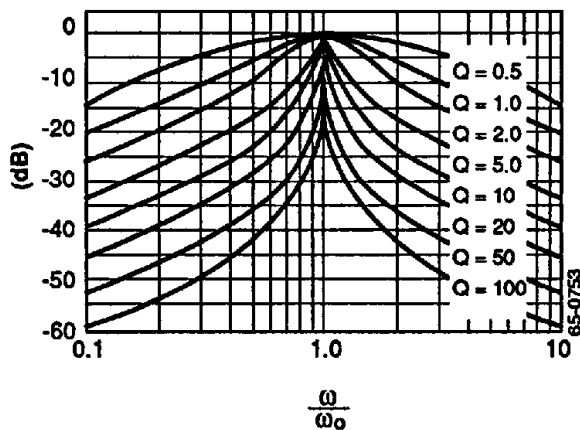
$$\frac{V_{BP}(s)}{V_{IN}(s)} = \frac{\frac{10^4}{R3} \omega_1 S}{S^2 + \left[ \frac{1.1 + \frac{10^4}{R3}}{1 + \frac{10^5}{R7}} \right] \omega_1 S + \frac{1}{\omega_1 \omega_2}}$$

This is now in a convenient form to look at the center-frequency  $\omega_0$  and filter Q.

$$\omega_0 = \sqrt{0.1 \omega_1 \omega_2} \quad \text{and } Q = \left[ \frac{1 + \frac{10^5}{R7}}{1.1 + \frac{10^4}{R3}} \right] \omega_0$$

$$\omega_0 = 10^{-9} \sqrt{0.1 R1R2}$$

The frequency response for various values of Q are shown in Figure 5.



$$\frac{V_{BP}}{V_{IN}} = \frac{\frac{\omega}{\omega_0} \frac{1}{Q}}{\sqrt{\left[1 - \left(\frac{\omega}{\omega_0}\right)^2\right]^2 + \left(\frac{1}{Q} \frac{\omega}{\omega_0}\right)^2}}$$

Figure 5. Bandpass Transfer Characteristics Normalized for Unity Gain and Frequency

These equations suggest a tuning sequence where  $\omega_0$  is first trimmed via R1 or R2, then Q is trimmed by varying R7 and/or R3. An important advantage of the state-variable bandpass filter is that Q can be varied without affecting center frequency  $\omega_0$ .

This analysis has assumed ideal op amps operating within their linear range, which is a valid design approach for a reasonable range of  $\omega_0$  and Q. At extremes of  $\omega_0$  and at high values of Q, the op amp parameters become significant. A rigorous analysis is very complex, but some factors are particularly important in designing active filters.

1. The passive component values should be chosen such that all op amps are operating within their linear region for the anticipated range of input signals. Slew rate, output current rating, and common-mode input range must be considered. For the integrators, the current through the feedback capacitor ( $I = C \, dV/dt$ ) should be included in the output current computations.

2. From the equation for Q, it should seem that infinite Q could be obtained by making R7 zero. But as R7 is made small, the Q becomes limited by the op amp gain at the frequency of interest. The effective closed-loop gain is being increased directly as R7 is made smaller, and the ratio of open-loop gain to closed-loop gain is becoming less. The gain and phase error of the filter at high Q is very dependent on the op amp open-loop gain at  $\omega_0$ .

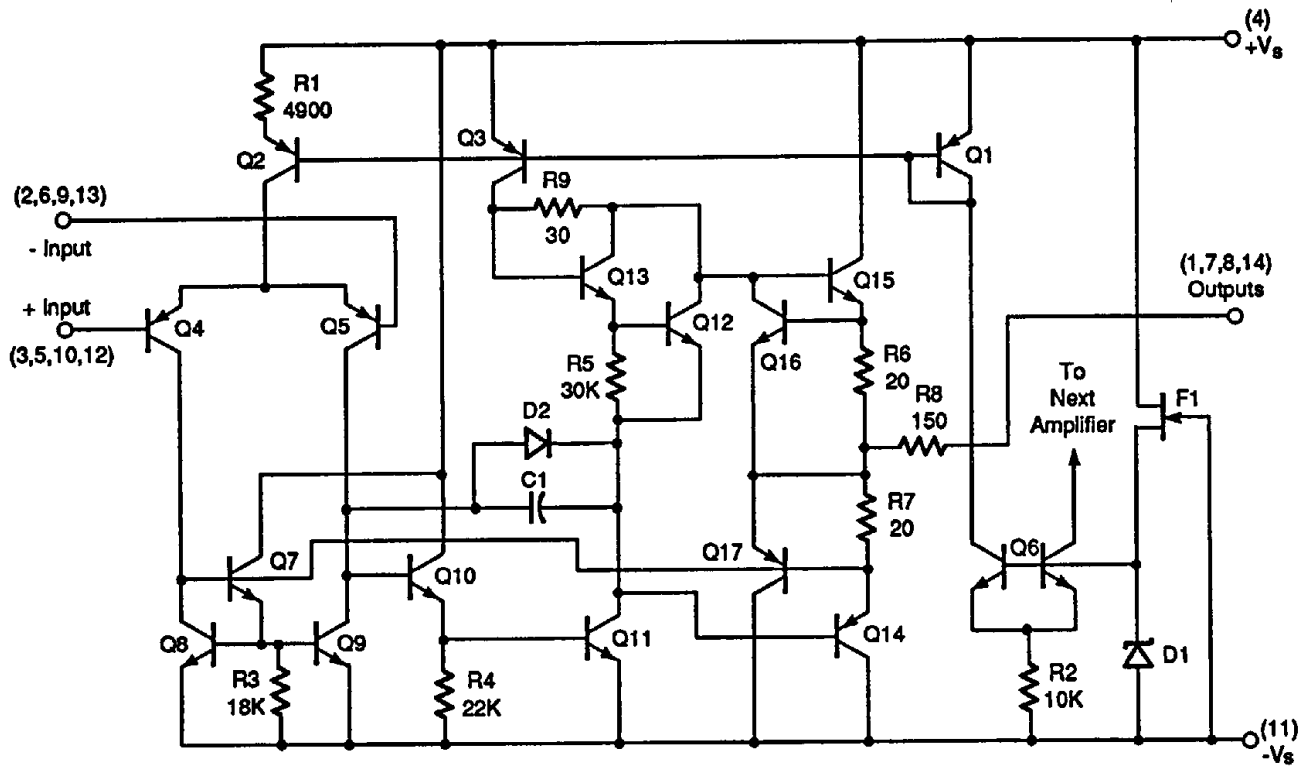
3. The attenuation at extremes of frequency is limited by the op amp gain and unity-gain bandwidth. For integrators, the finite open-loop op amp gain limits the accuracy at the low-end. The open-loop roll-off of gain limits the filter attenuation at high frequency.

The 4156 quad operational amplifier has much better frequency response than a conventional 741 circuit and is ideal for active filter use. Natural frequencies of up to 10 kHz are readily achieved and up to 20 kHz is practical for some configurations. Q can range up to 50 with very good accuracy and up to 500 with reasonable response. The extra gain of the 4156 at high frequencies gives the quad op amp an extra margin of performance in active-filter circuits.

# RC4156/RC4157

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Schematic Diagram (1/4 Shown)



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